Platform-driven ESL Design

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ESCUG 2006
Platform-driven ESL Design

- Model the architecture and validate
- Model the functionality visible to SW
- Model HW for optimization & verification

SystemC TLM Platform Modeling and Reuse
CoWare Solution Demos

SystemC Platform Capture and Analysis

- 1-2-3 assembly
  - Processor
  - Interconnect
  - Memory

- Simulation and analysis running MP3 decoder software
CoWare Solution Demos

SystemC Platform Capture and Analysis
- PV → OCP TL2

SystemC IP Block Development and Debug
- Standards-based TLM peripheral modeling and timing annotation
  - Using SCML

CoWare Platform Architect

CoWare Model Designer
CoWare Solution Demos

CoWare Platform Architect

SystemC Platform Capture and Analysis
- Back-end video processing subsystem

Bus Modeling and Configuration
- Dynamic configuration of OCP TL2 interconnect parameters and topologies

CoWare Model Library

and more…
CoWare Solution Demos

- CoWare Platform Architect
- SystemC Platform Capture and Analysis
  - Back-end video processing subsystem
- CoWare Processor Designer
- Programmable Accelerator Design
  - H.264 programmable accelerator design and integration
CoWare Solution Demos

CoWare Platform Architect

SystemC Platform Capture and Analysis
- WCDMA platform

CoWare Signal Processing Designer

Algorithm Design
- WCDMA encoder/decoder algorithm design and testbench integration
Thank you

Come see us at Booth B5